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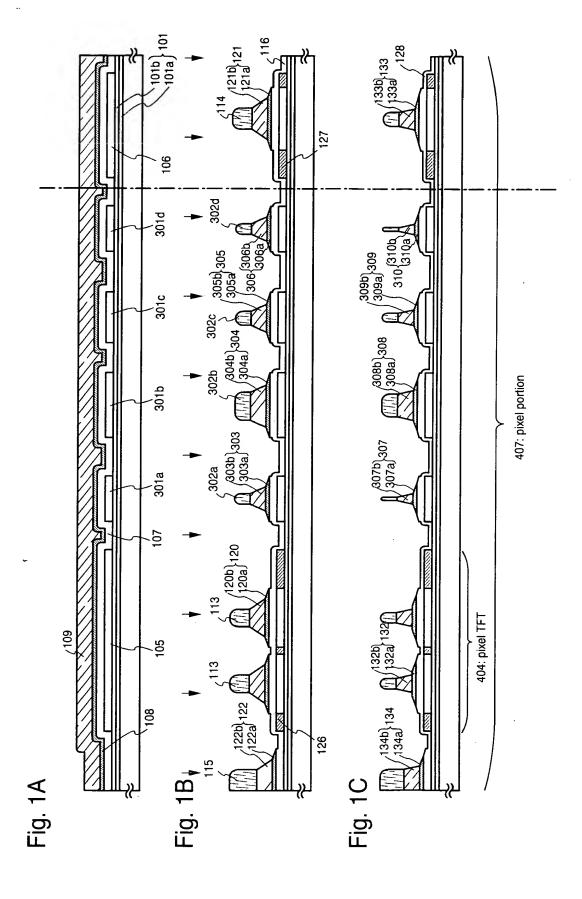
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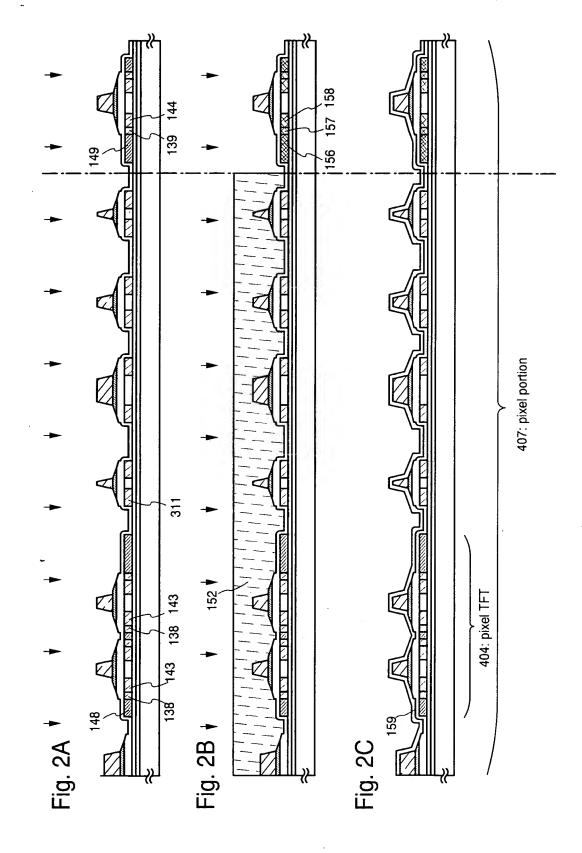
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702

404

407: pixel portion

ISSAISS OHENII

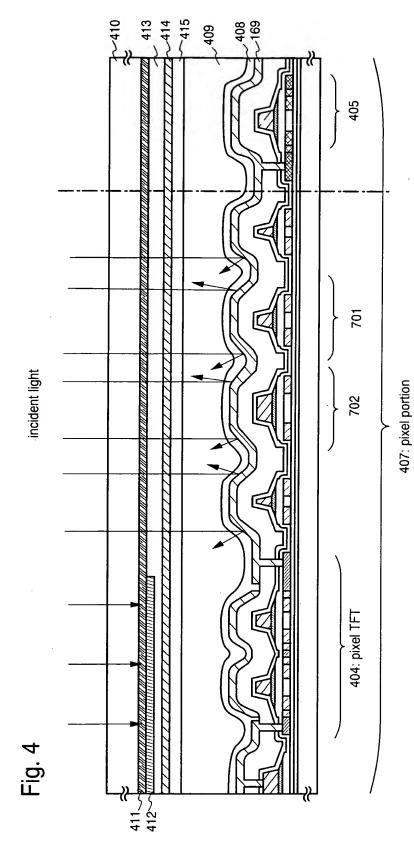
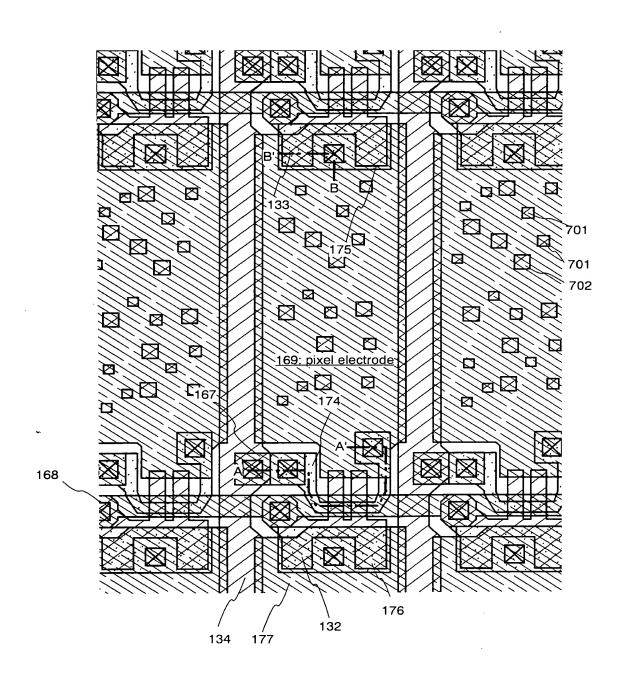


Fig. 5



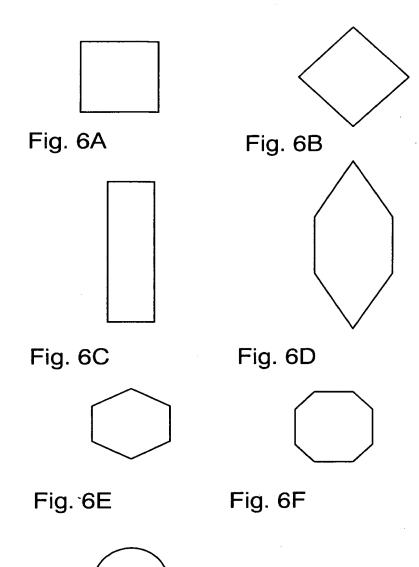
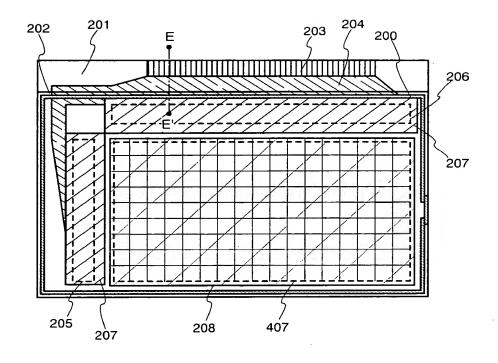
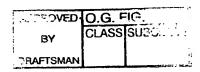


Fig. 6G

Fig. 7





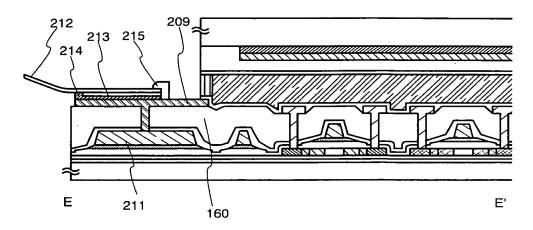


Fig. 8A cross sectional view of E-E'

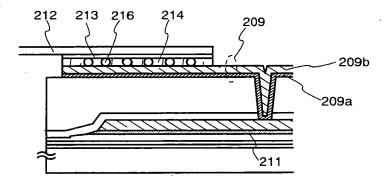


Fig. 8B

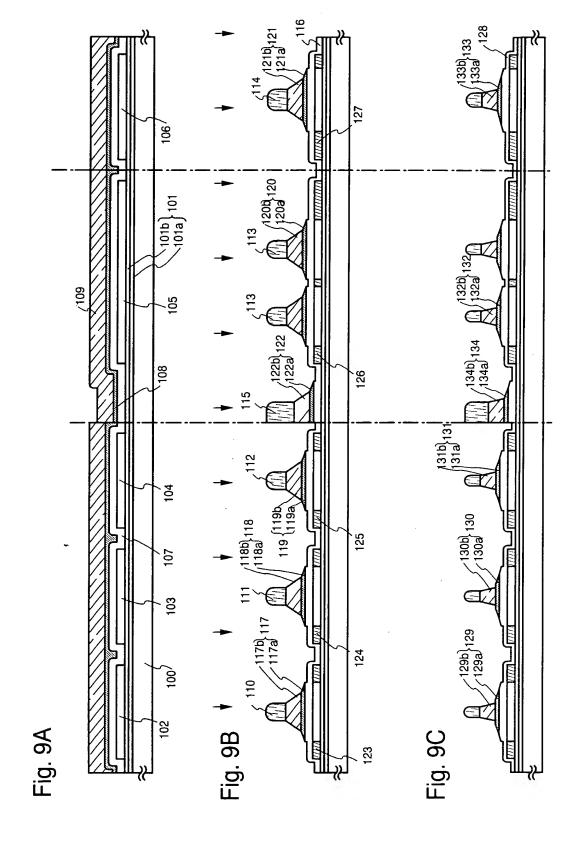


Fig. 11

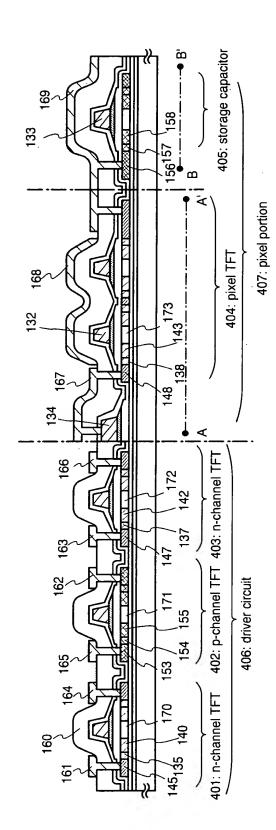
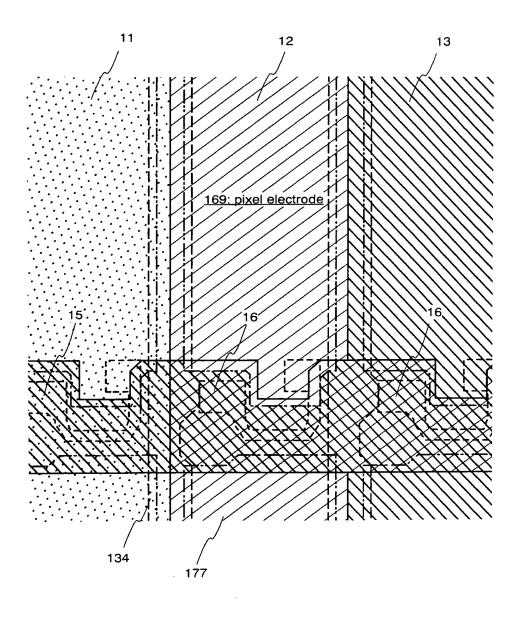


Fig. 12



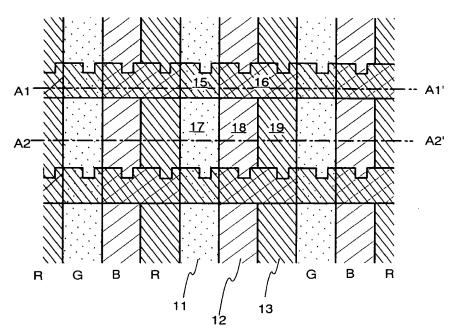


Fig. 13A

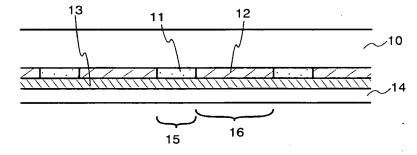


Fig. 13B cross sectional view of A1-A1'

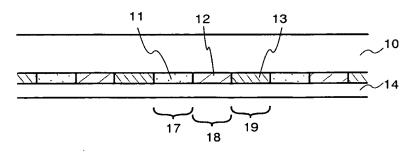
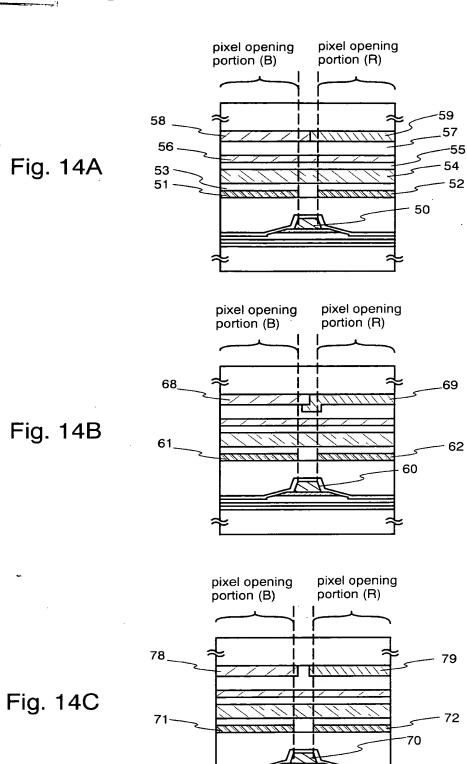


Fig. 13C cross sectional veiw of A2-A2'



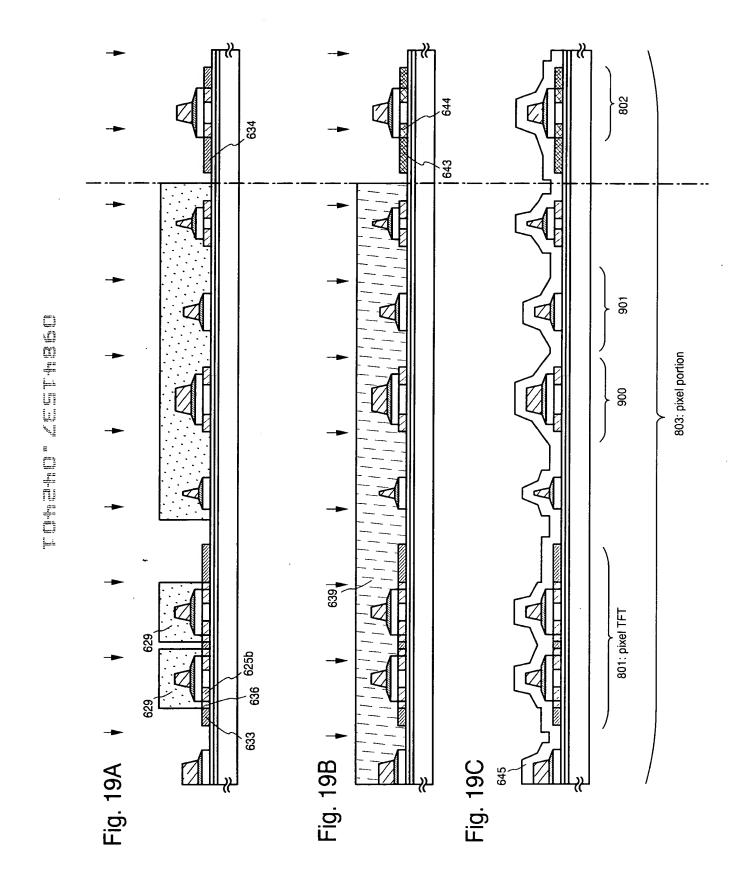


634 639 è25b 633 638 632 623b 642 635 640 631 637 630 Fig. 16A Fig. 16B Fig. 16C

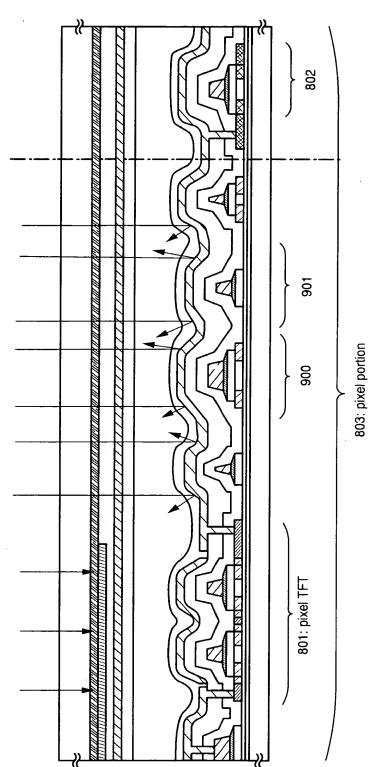
storage capacitor pixel portion pixel TFT 653 n-channel TFT 652 p-channel TFT driver circuit 651 650 n-channel TFT

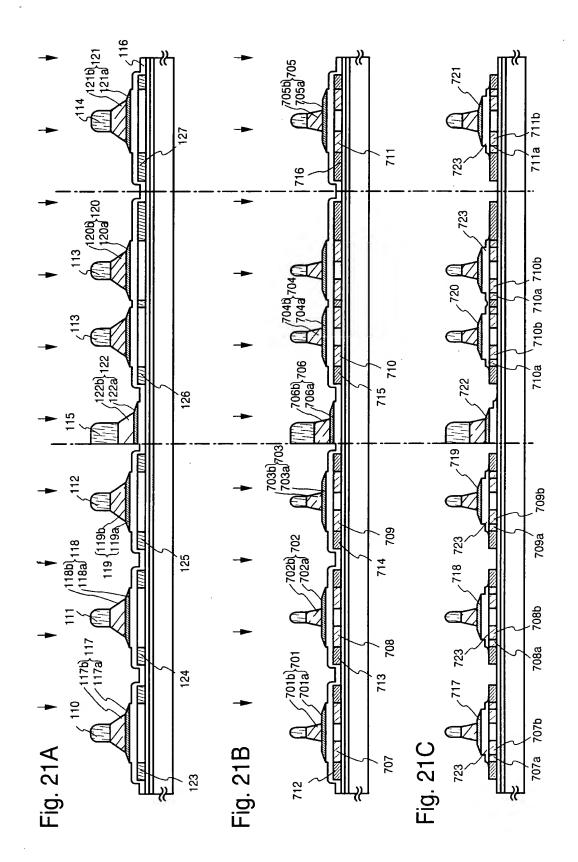
in 17

106 902d \101b\\101 600b 911 902c 906 (906b) 304 (905b) 905a) 905 908b) 908 908a) 902b 803: pixel portion 911 600a' 907b) 907a) 627b 108 Fig. 18C Fig. 18A Fig. 18B Fig. 18D



incident light Fig. 20





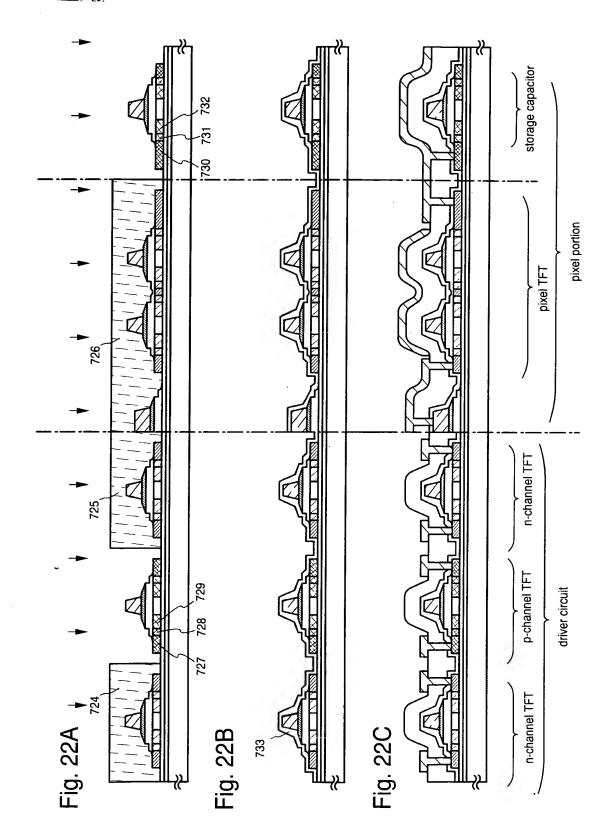
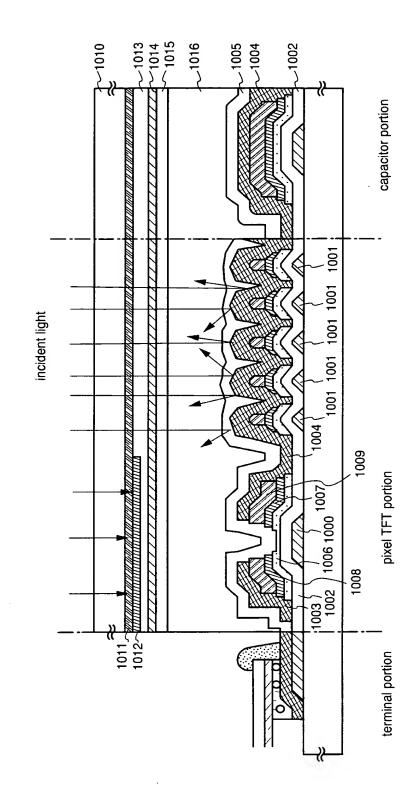


Fig. 23



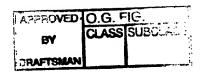
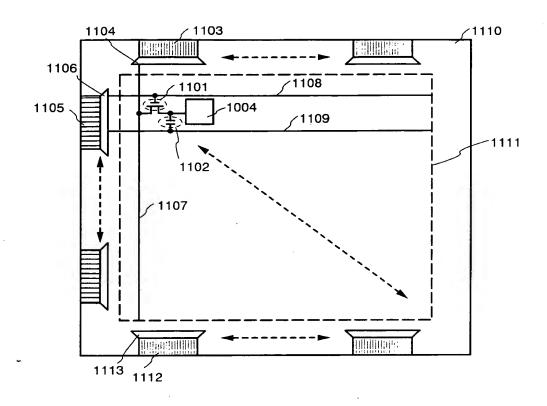


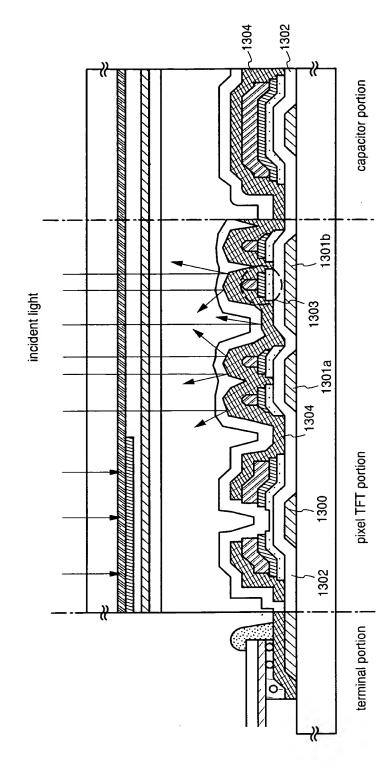
Fig. 24

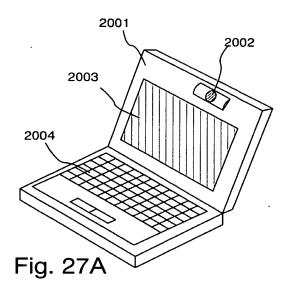


capacitor portion 1202 incident light }ॄ≅ 1200: pixel electrode pixel TFT portion terminal portion

Fig. 25

Fig. 26





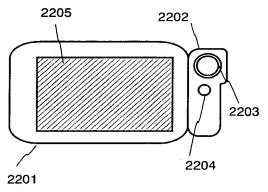


Fig. 27C

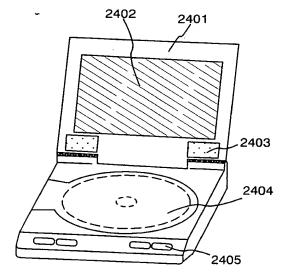
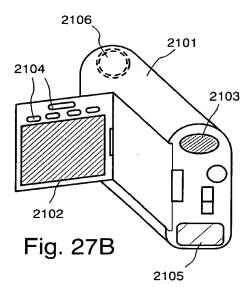


Fig. 27E



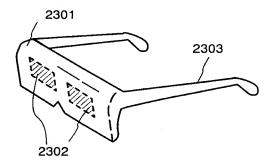


Fig. 27D

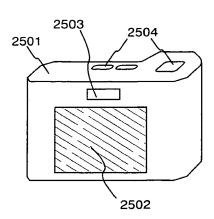


Fig. 27F

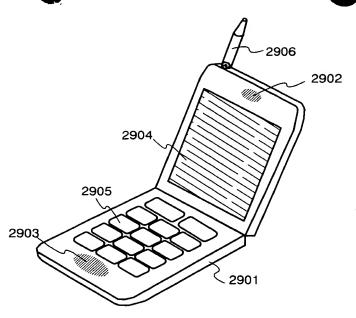


Fig. 28A

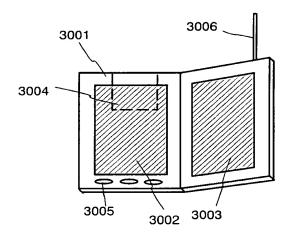


Fig. 28B

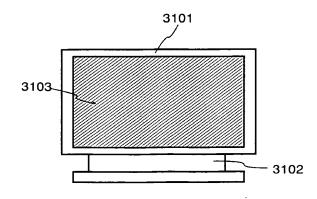
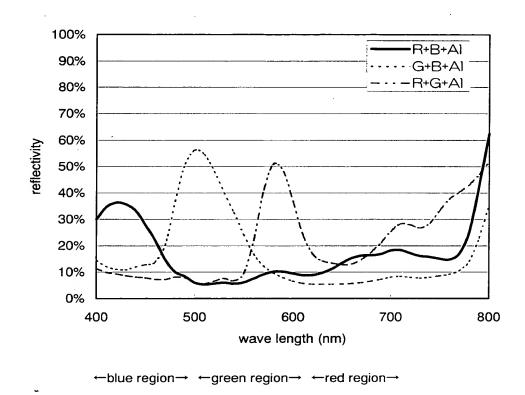


Fig. 28C

Fig. 29



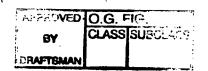
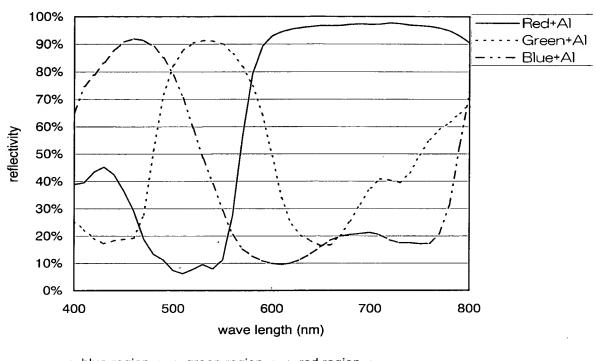


Fig. 30 prior art



←blue region \rightarrow ←green region \rightarrow ←red region \rightarrow